

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**APPLICANTS:** Sol P. DiJaili, Jeffrey D. Walker and John M. Wachsman

**APPLICATION NO.:** 10/020,558

**FILING DATE:** December 14, 2001

**TITLE:** Optical Logical Circuits Based on Lasing Semiconductor Optical Amplifiers

**EXAMINER:** Not yet known

**GROUP ART UNIT:** 2811

**ATTY. DKT. NO.:** 21153-05927

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, Washington, D.C. 20231, on the date shown below:

Dated: June 25, 2002

By: [Signature]

Michael D. Plimier, Reg. No.: 43,004

COMMISSIONER FOR PATENTS  
WASHINGTON, DC. 20231

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

**Under 37 CFR §§ 1.56 and 1.97-98**

SIR:

Pursuant to the provisions of 37 CFR §§ 1.56 and 1.97-98, enclosed herewith is modified form PTO-1449 listing references for consideration by the Examiner. Enclosed is a copy of each listed references that may be material to the examination of this application, and for which there may be a duty to disclose.

The filing of this Information Disclosure Statement shall not be construed as a representation regarding the completeness of the list of references, or that inclusion of a reference in this list is an admission that it is prior art or is pertinent to this application, or that a search has been made, or as an admission that the information listed is, or may be considered to be, material to patentability, or that no other material information exists, and shall not be construed as an admission against interest in any manner.

This Information Disclosure Statement is being filed:

- ☒ within three months of the filing date of the application, or date of entry into the national stage of an international application, or before the mailing date of a first office action on the merits, whichever event last occurred;

- ☐ before the mailing of a first official action after the filing of a request for continued examination (RCE) under 37 CFR § 1.114;
- ☐ after three months of the filing date of this national application or the date of entry of the national stage in an international application, or after the mailing date of the first official action on the merits, whichever event last occurred, but before the mailing date of the first to occur of either: (1) a final action under 37 CFR § 1.113; or (2) an action that otherwise closes prosecution in the application, and:
  - ☐ attached hereto is the fee set forth under 37 CFR § 1.17(p) for submission of this Information Disclosure Statement under 37 CFR § 1.97(c); OR
  - ☐ Applicant certifies pursuant to 37 CFR § 1.97(e) that:
    - ☐ each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; OR
    - ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of the person signing this certification after making reasonable inquiry, no item of information contained in this Statement was known to any individual designated under 37 CFR § 1.56(c) more than three months prior to the filing of this Statement;
- ☐ before the payment of the issue fee but after the mailing date of the first to occur of either: (1) a final action under 37 CFR § 1.113; or (2) an action that otherwise closes prosecution in the application, and:
  - ☐ Applicant certifies pursuant to 37 CFR § 1.97(e) that:
    - ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; or
    - ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a

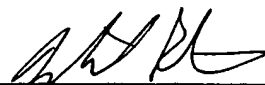
foreign patent office in a counterpart foreign application and, to the knowledge of the person signing this certification after making reasonable inquiry, no item of information contained in this Statement was known to any individual designated under 37 CFR § 1.56(c) more than three months prior to the filing of this Statement; AND

- ☐ attached hereto is the fee set forth under 37 CFR §1.17(p) for submission of this Information Disclosure Statement under 37 CFR. § 1.97(c); OR
- ☐ after the payment of the issue fee. Applicant request that the information contained in this Information Disclosure Statement be placed in the file according to 37 CFR § 1.97(i), although the information may not be considered by the USPTO.
- ☐ This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior application No. \_\_\_\_\_, filed on \_\_\_\_\_, and the references cited therein are hereby referenced, but are not required to be provided in this application under 37 CFR § 1.98(d).
- ☐ Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application, and the communication was not received by any individual designated in 37 CFR § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement. 37 CFR § 1.704(d).
- ☒ Applicant submits that no fee is required for the consideration of this Information Disclosure Statement.

Consideration of the listed references and favorable action are solicited.

Respectfully submitted,  
Sol P. DiJaili, Jeffrey D. Walker  
and John M. Wachsman

Dated: June 25, 2002

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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
Patent and Trademark Office

Attorney's Docket No.

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Serial No.

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## INFORMATION DISCLOSURE CITATION

Applicants

Sol P. DiJaili

Filing Date

December 14, 2001

Group Art Unit

2811

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	R1	5,805,322	09-08-98	Tomofuji	359	177	
	S1	5,778,132	07-07-98	Csipkes, et al.	385	135	
	T1	5,754,571	05-19-98	Endoh, et al.	372	20	
	U1	5,604,628	02-18-97	Parker, et al.	359	344	

## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	V1	Walker, J.D., Patterson, F.G., DiJaili, S.P. and Deri R.J., "A Gain-Clamped, Crosstalk Free, Vertical Cavity Lasing Semiconductor Optical Amplifier for WDM Applications", Lawrence Livermore National Laboratory, Livermore, CA

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if references considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.

PTO-1449

REV: 02/01

21153/05927/DOCS/1278194.1